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Michael J. McGovern  
Attorney of Record



**PATENT**

Docket No. 121812.00003

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Ju Yan Pan  
Appl. No.: 10/727,910  
Filed: December 4, 2003  
Title: A METHOD OF CALCULATING INTERNAL SIGNALS FOR USE IN A MAP ALGORITHM  
Art Unit: 2124  
Examiner: ---

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**INFORMATION DISCLOSURE STATEMENT**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please enter the following information in the file of the above application for consideration during examination of this application. Attached is a Form PTO/SB/08A, which lists the items of prior art which came to the attention of the undersigned. A copy of these items are submitted herewith.

It is hoped that this submission of prior art will assist the examination.

Respectfully submitted,

By: Michael J. McGovern

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/727,910
Filing Date	December 4, 2003
First Named Inventor	Ju Yan Pan
Art Unit	2124
Examiner Name	---
Attorney Docket Number	121812.00003

Sheet 1 of 1

### OTHER PRIOR ART–NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Berrou, et al., "Near Shannon Limit Error-Correcting Coding and Decoding:Turbo-Codes," in Proceedings of ICC'93, Geneva, Switzerland, ppg. 1064-1070, May 23-25 1993.	
		Warren J. Gross and P. Glenn Gulak, "Simplified MAP Algorithm Suitable for Implementation of Turbo Decoders," Department of Electrical and Computer Engineering, University of Toronto, Toronto, Canada.	
		Chass, et al., "Efficient Software Implementation of the Max-Log-MAP Turbo Decoder on the StarCore SC140 DSP," Motorola Semiconductor Israel Ltd, Shenkar 1, Herzelia, Israel.	
		Patrick Robertson and Peter Hoeher, "Optimal and Sub-Optimal Maximum a Posteriori Algorithms Suitable for Turbo Decoding," European Transactions on Telecommunications, vol. 8, no. 2, March-April 1997, pp. 119-125.	
		EG TS 25:212 V3.3.0 (2000-06), "3rd Generation Partnership Project; Technical Specification Group Radio Access Network; Multiplexing and channel coding (FDD) (Release 1999) pgs. 1-62.	
		IEEE Transactions on Information Theory, March 1974, pgs. 284-287.	

Examiner  
Signature

Date  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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